AN IN-MEMORY COMPUTING SERIES

Next Talk: 21/June/2021, 4-5:30pm CET

DEEP LEARNING ACCELERATION: A KILLER APPLICATIONFOR IN-MEMORY COMPUTING?

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The rise of AI and in particular, deep learning (DL), is a key driver for innovations in computing systems. There is a significant effort towards the design of custom ASICs based on reduced precision arithmetic and highly optimized dataflow. However, the need to shuttle millions of synaptic weight values between the memory and processing units, remains unaddressed. In-memory computing (IMC) is an emerging computing paradigm that addresses this challenge of processor-memory dichotomy. Attributes such as synaptic efficacy and plasticity can be implemented in place by exploiting the physical attributes of memory devices such as phasechange memory (PCM). In this talk, I will give a status update on where in-memory computing stands with respect to DL acceleration. How do we tackle imprecision arising from noisy, analog computing? I will present some recent algorithmic as well as device-level innovations. I will also touch upon some system-level aspects and will present a world's first IMC compute core based on PCM fabricated in 14nm CMOS technology. Finally, I will provide a brief overview of photonic in-memory computing that could facilitate unprecedented latency and compute density.

More information about the event and the speaker: https://www.ict.tuwien.ac.at/staff/taherinejad/MiM/next.html

Mondays in Memory (MIM) is a free biweekly webinar series open to everyone around the world and dedicated to all aspects and technologies related to in-memory computing (including, in a broader sense, near-memory computing too). MIM will be held on the first and third Monday of each month (starting in May 2021) at 4pm CET (7am Pacific time, and 10pm Beijing time).

Each webinar starts with a 40mins talk by a speaker, followed up with a 40mins questions and discussions with the speaker and two panel members. Dr. Nima Taherinejad hosts the webinars, and together with his team they organize the MiM series.

Website: http://www.ict.tuwien.ac.at/ staff/ aherinejad/MiM/ Email:nima.taherinejad@tuwien.ac.at

Abu Sebastian is a Distinguished Research Staff Member at IBM Research – Zurich. He was



a contributor to several key projects in the space of storage and memory technologies and currently leads the research effort on in-memory computing at IBM Zurich. Dr. Sebastian is a co-recipient of the 2009 IEEE Control Systems Technology Award and the 2009 IEEE Transactions on Control Technology Systems Outstanding Paper Award. In 2015 he was awarded the **European Research Council** (ERC) consolidator grant and in 2020, he was awarded an ERC Proof-of-concept grant. He is an IBM Master Inventor since 2016. He was named Principal and **Distinguished Research Staff** Member in 2018 and 2020, respectively. In 2019,he received the **Ovshinsky** Lectureship Award for his contributions to "Phase-change materials for cognitive computing".